

Date of publication xxxx 00, 0000, date of current version xxxx 00, 0000.

Digital Object Identifier 10.1109/ACCESS.2017.DOI

Digital Electrical Substation Communications based on Deterministic Time-Sensitive Networking over Ethernet

J. SANCHEZ-GARRIDO¹, A. JURADO¹, L. MEDINA², R. RODRIGUEZ², E. ROS¹, AND J. DIAZ¹

¹Computer Architecture and Technology Department, University of Granada, Granada, 18071, Spain

²R&D Division, Seven Solutions S.L., Granada, 18014, Spain

Corresponding author: J. Sanchez-Garrido (e-mail: jorgesg@ ugr.es).

The authors would like to thank Alberto Sánchez Pérez and *Grupo Cuerva S.L.* for their assistance in the realization of the field tests at their electrical substation facility in Escúzar (Granada, Spain); and Jesús Torres Tenor and the CIRCE Foundation for their contribution to the development of a substation GOOSE traffic generator. This Project has been partially supported by the Amiga-7 (RTI2018-096228-B-C3) project, the FITOPTIVIS (H2020-RIA ECSEL-JU-2017-783162) project, the Spanish MINECO grant (APCIN PCI2018-093184), and the Spanish FPU PhD Programme grant.

ABSTRACT This work presents a novel use case with Time-Sensitive Networks (TSN) for implementing a deterministic system allowing the joint transmission of all substation communications over the same Ethernet-based infrastructure. This approach streamlines the transition to Smart Grid by simplifying the typically complex architecture of electrical substations, characterized by multiple field buses and bridging devices. Thus, Smart Grid represents a disruptive innovation advancing substations to an “all-digital” environment with a uniform interface to access, manage, and update their communications and variables. TSN can serve as its underlying foundation as it is based on open, interoperable standards and enhancements for Ethernet that can establish deterministic communications with bounded end-to-end latency. This is shown with a TSN Proof of Concept (PoC) in a real-life substation that can integrate its most usual signals: digitized analog triggers for critical events or interlocks, GOOSE signaling (IEC 61850), and Best-Effort “Internet-like” traffic. This TSN PoC is shown to be versatile enough to propagate digitized critical events around 160 μ s earlier than legacy substation equipment while preserving the integrity of background traffic. Furthermore, its flexibility was characterized in-depth in controlled laboratory tests, thereby confirming TSN as a viable alternative for supporting Smart Grid so long as the appropriate configuration is supplied.

INDEX TERMS Deterministic Communications, Digital Substation, Smart Grid, TSN

I. INTRODUCTION. SMART GRID AND THE MOTIVATION FOR THE APPLICATION OF TSN NETWORKS

This paper describes the application of a Time-Sensitive Networking (TSN) system to support the deployment of Smart Grid features in electrical substation environments. Time-Sensitive Networking is conceived as a number of enhancements for regular Ethernet networks [1] allowing the coexistence of data flows with different levels of criticality, bounded end-to-end latency, guaranteed bandwidth, and ensured determinism for critical traffic. Hence, these networks, which are based on interoperable open standards, implement so-called convergent communication systems that allow the aggregation of different flows according to user-defined criteria.

The concept of Smart Grid emerges from the application of

new techniques and processes to legacy power grids replacing their traditional hierarchy with a completely integrated environment to efficiently process system services, exchange process data, and forward system transactions. Hence, its implementation is an ongoing transformation on all the levels of the power grid (from the generation to the distribution stages) that is being driven by the need to provide an efficient communication layer for accommodating the management of new energy sources. This results in power supply infrastructure with improved fault tolerance, and enhanced safety and quality [2], [3].

On the distribution level, this upgrade is aimed at digitizing the different control and communication equipment of substations handling the operation of the power distribution subsystems, including their sensors and actuators. A funda-

mental aspect of this transformation is the communication technology amongst the different substation processes, which will be replaced by Ethernet networks (either on optical fiber or copper-based links), instead of using vendor-specific solutions.

These Ethernet networks have the potential of allowing a streamlined exchange of data and the establishment of redundant communication paths for critical flows but cannot enforce any delivery guarantees or ensure end-to-end determinism. Thus, in a substation scenario, this traffic is usually handled with a three-tier architecture supported by dedicated field buses. These tiers are the *Field Level*, which implements lower-level interfaces with sensors and actuators; the *Bay Level*, which includes the equipment controlling the operation of the substation; and the *Substation Level*, which defines communication interfaces with other elements from the power grid.

Thus, the Field-level processes usually have to handle time-critical traffic, such as event alarms, trigger signals, or the interlocking mechanisms of closed control loops. This type of traffic is usually propagated using the IEC 61850 standard [4] protocol, which carries these messages from the Merging Units (*MUs*) of the substation to the Intelligent Electronic Devices (*IEDs*), where the corresponding monitoring, control, protection, and diagnostics tasks of the substation are implemented.

For its application in Smart Grid, the IEC 61850 protocol needs to be complemented with additional features; such as node discovery, reconfiguration, aggregation of different priority flows, or guaranteed determinism as bounded end-to-end latency. Additionally, its messages have to be transmitted using Ethernet-based networks. There are several Ethernet-based approaches that can be applied for Smart Grid, such as PRP (*Parallel Redundancy Protocol*) or White Rabbit (WR) HSR (*High-Availability Seamless Redundancy*) [5]. The former allows the creation of redundant network topologies, whereas the latter uses a customized implementation of the White Rabbit synchronization stack to distribute timing information and to implement redundant ring topologies with $\sim 3 \mu\text{s}$ recovery times [3]. However, none of these systems can enforce any type of message delivery guarantee. Hence, TSN represents the ideal alternative to support the migration to Smart Grid, as it can implement a deterministic communication network with support for redundant ring topologies for select flows, as mandated by IEC 61850, with zero switchover time using its 802.1CB [6] component.

Consequently, this paper shows that TSN is a viable alternative for supporting substation communication flows in accordance with the specifications of IEC 61850 [7]. The operation of the system is characterized in-depth in the following sections, but further reliability studies can still be conducted following the guidelines proposed in [8]. Therefore, after introducing the motivations for the adoption of TSN, its main functionalities and components are described in Section II. Next, the architecture of the TSN network nodes used in this work is explained in Section III. Then, the feasi-

bility of applying a TSN network for integrating Smart Grid communications is shown in Section IV with the deployment of an experimental Proof-of-Concept (PoC) setup in a real-life substation. The results of the initial PoC are further characterized with a laboratory test bench in Section V, where the effects of applying different configuration parameters can be studied. Lastly, Section VI concludes by showing the viability of applying TSN for Smart Grid. The effects of user-driven configuration on achievable determinism are also outlined, and future lines of work are presented, such as the development of a specific TSN profile for Smart Grid.

II. TIME-SENSITIVE NETWORKING (TSN)

This section is intended to give the reader a brief overview of the concept of Time-Sensitive Networking (TSN) and its application on Ethernet-based networks. Readers already familiar with the concept and operation of TSN technologies may directly proceed to Section III.

TSN emerges as a set of enhancements for Ethernet networks set forth by the IEEE Standardization Committees. Traditionally, Ethernet has been considered a robust technology that can fulfill the communication needs of general-purpose applications and, as a result, its use became pervasive alongside the widespread deployment of the Internet. It is thus a well-known standard that has become the foundation of the so-called “*open world*” communications. These are the IP-based flows that make up the bulk of most of today’s popular Internet applications; such as HTTP web browsing or audio/video streaming. Typical Internet traffic is Best-Effort (BE) in nature and is usually supported with Ethernet networks, which share the same service philosophy.

The scenarios where timing and determinism guarantees are required usually rely on specialized, vendor-specific solutions. This is the case of industrial plants, avionics systems, or sensor-actuator control loops in general. These implement “*closed world*” communications and typically make use of field buses, such as ModBus or CAN, to guarantee that their timing and real-time constraints can be met. In heterogeneous environments, their use can lead to complex architectures with different bus domains, hence making their integration and maintenance highly costly. Furthermore, some scenarios may even require a separate data network, which adds up to system complexity.

TSN allows the combination of the aforementioned Internet-like, “*open world*” communications, with the “*closed world*” traffic associated with industrial monitoring and control using the same standard-based Ethernet network. An initial approach to this was the development of AVB (Audio/Video Bridging) [9], which was geared towards professional multimedia environments. Later on, AVB was superseded and targeted to broader applications including industrial automation, hence giving rise to Time-Sensitive Networking. Its components are outlined next.

- **System-wide Synchronization** provides a common time reference for the network. This allows the nodes of a distributed TSN system to work synchronously

and forward messages consistently with the time of the network. This reference is maintained by the gPTP service (802.1AS) [10], which implements a PTP profile tailored for TSN. In the context of Smart Grid, gPTP timing can provide synchronization on the order of tens of nanoseconds (experiment V-C1) for the substation equipment, far exceeding the $\sim 1 \mu\text{s}$ specification of IEC 61850 Part 5 [11]. As this reference will be distributed over Ethernet links, it is a safer alternative than the GPS-based systems that synchronize Phasor Measurement Units in some substations, adding protection against accidental or intentional GPS malfunction [12].

- **Bounded End-to-End Latency.** TSN networks define different queueing and forwarding mechanisms for guaranteeing end-to-end latency. The Time-Aware Traffic Shaper (TAS) [802.1Qbv] [13] stands out, as it forwards traffic according to a Gate Control List (GCL) schedule supplied by the user. This is complemented with the enhancements for preemption of lower priority messages in favor of critical, express traffic (802.1Qbu & 802.3br) [14], [15]. Their action allows TSN systems to guarantee system-level determinism for critical messages and the coexistence with Best-Effort data in the same Ethernet network.
- **System Configuration and Traffic Identification.** TSN handles traffic according to its *Traffic Class*. Thus, it implements several mechanisms for specifying the network topology, the criteria for assigning messages to a given traffic class, or the GCL Schedule of the traffic shapers. This is accomplished with resource reservation protocols (802.1Qcc, 802.1BA) [16], [17], which disseminate these parameters throughout the system. The traffic classes identified with these parameters will be denoted with a VLAN-tagged TSN stream (802.1Q) [18], whose associated priority will indicate the forwarding queue of the TAS module that it will be assigned to.
- **Reliability. Seamless Redundancy.** TSN networks can make use of the 802.1CB [6] component for protecting highly critical messages by allowing their transmission over disjoint physical paths in the network using a standard-defined frame replication scheme. This feature is often required in scenarios where the delivery of time-critical messages has to be guaranteed. This is the case of Smart Grid, where the use of redundancy mechanisms is expected in IEC 61850.

This work shows the application of a TSN approach for Smart Grid using two purpose-built nodes, whose architecture is described in Section III.

III. IMPLEMENTATION OF A TSN-CAPABLE NETWORK NODE

The experiments presented in this work make use of two different TSN nodes: the WR-ZEN board and the MAIN TSN Switch. They are both based on the Zynq-7000 devices from Xilinx [19]. These devices are *programmable Systems-on-*

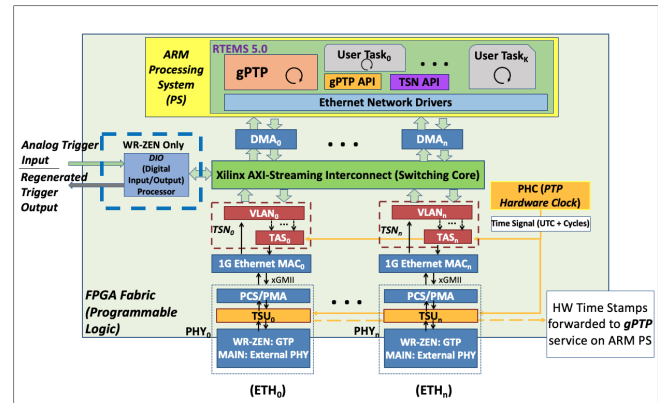


FIGURE 1: General System Architecture diagram for the TSN nodes used in this work, highlighting the different subsystems within the FPGA Programmable Logic and their interactions.

Chip (SoCs), featuring a dual-core ARM Processing System (PS) for running an embedded OS (e.g., Linux, RTEMS [20]), and FPGA Programmable Logic (PL) for implementing HDL coprocessors that can interface with the PS. These two TSN nodes share the same underlying architecture but use different SoC devices depending on their role in the system.

A. THE TSN NETWORK NODES

- **The WR-ZEN Node** implements a two-port network device that can operate both as a TSN *Listener* (Receiver) or a TSN *Talker* (Transmitter) of highly critical messages. Hence, it implements a reduced TSN system in the relatively small, low-cost Z-7015 programmable SoC. Additionally, this node includes a dedicated HDL coprocessor (*DIO*) for digitizing analog substation triggers in order to forward them over the network as high priority TSN messages. This node repurposes the original architecture of the WR-ZEN board [21], which was originally used for distributing White Rabbit timing [22].
- **The MAIN TSN Switch.** This node implements a four-port *TSN bridge* that forwards different TSN messages amongst its ports in accordance with user-specified settings. The bridge functionality requires the use of a greater amount of FPGA resources to support a multi-port implementation. This led to a design that uses the larger Z-7030 Xilinx device, which was integrated in the purpose-built MAIN circuit board.

B. TSN NODE ARCHITECTURE

Fig. 1 shows the common architecture for the foregoing nodes. It features both FPGA subsystems and software-based components. The FPGA subsystems are implemented in the PL of the corresponding Zynq-7000 device and consist of several units that interact with one another. These are the Ethernet Networking Subsystem (blue), the Timing Distribution Subsystem (orange), the switching cores (green), and the

TSN Subsystem (red). The *DIO coprocessor* is an exclusive component of the WR-ZEN node and is highlighted with a blue box. The software components run on the ARM-based PS and control the operation of the aforementioned subsystems. These components include the RTEMS OS, the gPTP service, configuration APIs, or other user tasks.

- **The Ethernet Networking Subsystem** provides the underlying Ethernet service for establishing a functioning TSN network. It consists of several off-the-shelf components allowing the instantiation of ordinary Ethernet ports in the PL of the Zynq-7000 device. These components include DMA controllers, a lightweight 1G Ethernet MAC ported from an open core design [23], the Xilinx PCS/PMA core, and Ethernet transceivers (GTP blocks for the WR-ZEN or a dedicated PHY chip for the MAIN node).
- **The Timing Distribution System (gPTP)** provides the crucial timing synchronization required for any TSN system as specified in the 802.1AS subcomponent [10]. This component defines an implementation of PTP particularized for TSN, which was applied to Smart Grid using an FPGA-based design like that shown in [24]. Thus, the subsystem requires two main cores in the FPGA logic that will be coordinated by a system service of the PS implementing the gPTP protocol: The PTP Hardware Clock (PHC) and the Time-Stamping Units (TSUs). The PHC contains the internal time representation of the node, which is steered by the execution of the gPTP synchronization service. The TSUs will be used for retrieving time stamps associated with the exchange of gPTP protocol messages.
- **The TSN Subsystem** implements the essential elements allowing the establishment of deterministic communication flows. In Fig. 1, these elements, which are instantiated on a per port basis, are the TSN VLAN Core and the Time-Aware Traffic Shaper (*TAS*). The VLAN Core operates as an input and forwarding stage: it identifies different types of traffic according to user-defined criteria, which are then encapsulated into VLAN-tagged TSN streams and delivered to the appropriate port and queue over the AXI Switching Core. The Time-Aware Traffic Shaper works synchronously with the time reference supplied from the PHC. It forwards messages deterministically by periodically activating its priority queues according to a user-defined Gate Control List (GCL) Schedule.
- **The Software Environment of the TSN nodes.** The TSN nodes make use of Zynq-7000 devices with a dual-core Processing System that supports the execution of the embedded real-time RTEMS 5.0 OS [20] environment. This OS provides the framework to support the various software components, modules and services to handle the operation of the TSN system: a gPTP synchronization service, Ethernet network drivers, and configuration APIs. The synchronization service up-

dates the time representation of the PHC with the gPTP protocol (802.1AS [10]) and was developed as a custom RTEMS port of the OpenAvnu Project [25]. The network drivers initialize the Ethernet Subsystem and were adapted into RTEMS from the Xilinx Ethernet Network drivers [26]. They were additionally customized to allow their interaction with the Timing Subsystem. Lastly, the system uses two different APIs. The gPTP API is used to pass configuration parameters to the gPTP synchronization service (e.g., oscillator quality). The TSN API is used to indicate the traffic classes and applicable GCL schedules to the TSN Subsystem.

C. CONSIDERATIONS ON FPGA FOOTPRINT

These experiments make use of two different flavors of TSN nodes: The WR-ZEN node and the MAIN TSN node. These are based on Zynq-7000 devices and have different resource requirements depending on their role in the network. Thus, while the former implements an End-System with just two Ethernet ports, the latter is a four-port bridge with advanced switching capabilities. The system architecture of Fig. 1 shows that some cores are instantiated on a per port basis, and that others have FPGA slice usage dependent on their number of bus interfaces, as is the case of the AXI Switching Core. Hence, resource consumption is bound to be dependent on the number of ports instantiated in a particular node. Consequently, the WR-ZEN node will require relatively reduced FPGA logic and can be fitted in the small, low-cost Z-7015 device (70% overall usage), whereas the MAIN node will require greater resources and will have to use the larger Z-7030 device (60% overall usage) to support its four-port design. These figures were achieved with a design that prioritized moderate FPGA footprint, which led to some compromises in the implementation, like the use of relatively modest 4 kB buffers for each queue in the traffic shapers to reduce the utilization of Block RAM primitives.

IV. EXPERIMENTAL VALIDATION. ELECTRICAL SUBSTATION FIELD TESTS

The main premise of this work is to show that a TSN system can manage to successfully integrate all the data flows of electrical substations, especially the critical ones, over Ethernet networks extending the capabilities of the communication buses currently described in the IEC 61850 standard. Specifically, the proposed TSN system can implement the underlying communication layer of a Smart-Grid-enabled substation and, as a key difference from existing approaches, it allows for the joint transmission over shared physical links of internal control signaling [27], monitoring messages, and digitized analog triggers associated with critical events, which typically required the use of dedicated analog interfaces. Consequently, a Proof-of-Concept system demonstrating the feasibility of the application of TSN for Smart Grid was deployed in the real-life substation facility of the local electricity provider Grupo Cuerva S.L. [28] in Granada (Spain). This facility features a typical substation

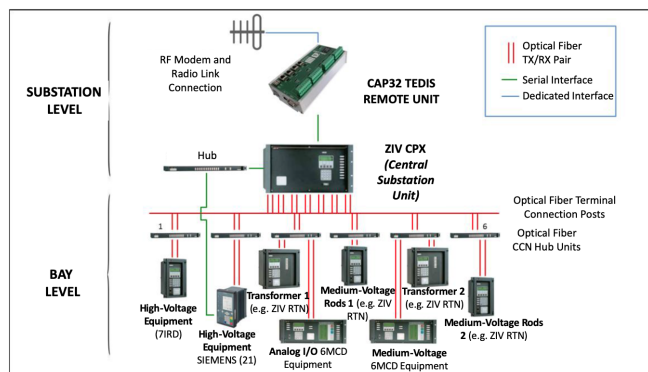


FIGURE 2: Simplified system diagram of components and architecture in an electrical substation from Grupo Cuerva S.L., where the field tests were conducted.

environment for performing a number of field tests for the TSN PoC, and its components can be examined in Fig. 2.

A. THE SUBSTATION ENVIRONMENT

The substation architecture consists of a central control element, the CPX Substation Central Unit from ZIV Automation [29], that interfaces with the rest of the equipment in the facility. The CPX Substation Central Unit operates on the Substation Level and is therefore tasked with implementing a number of supervisory and control processes over the rest of the equipment operating on the lower Bay Level. Units such as the Transformer Control Unit (ZIV RTN) or the Line Protection Unit (7IRD) stand out amongst the elements in this latter level. The supervisory tasks over these units are supported using dedicated TX/RX optical fiber link pairs. These links do not implement a proper Ethernet network, but are rather used to efficiently implement an electrically isolated communication channel. Additionally, the entire substation can be remotely managed using a dedicated RF link that interfaces with the CPX unit.

B. THE SUBSTATION FIELD TESTS

The field tests conducted in this work consisted of the deployment of a TSN system operating on the Bay level of the substation facility in order to evaluate the performance of the application of a TSN network for the transmission of highly critical data, GOOSE control signaling, and Best-Effort messages. To this end, the experimental TSN demonstrator interconnected the 7IRD Line Protection Unit with other substation equipment, as shown in the diagram of the PoC system of Fig. 3 or the picture of the actual deployment in the substation facility of Fig. 4.

These tests made use of two main switching devices (the MAIN TSN nodes), and two end-systems (the WR-ZEN nodes) for establishing the demonstrator TSN network. In this setup, one of the WR-ZEN nodes assumes a TSN Talker role (ZEN-Pub) and is tasked with digitizing the analog triggers produced at the 7IRD unit and forwarding them as critical TSN messages. These messages will be received at

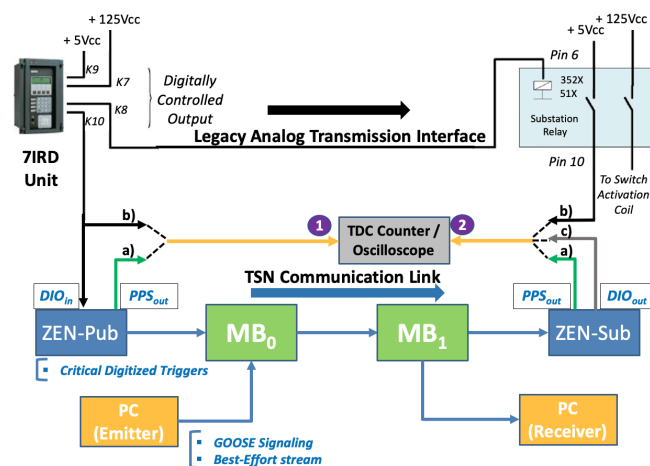


FIGURE 3: Diagram of the experimental TSN network deployed in the substation, highlighting its application for transmitting digitized signals originating from the 7IRD unit as high-priority TSN messages.

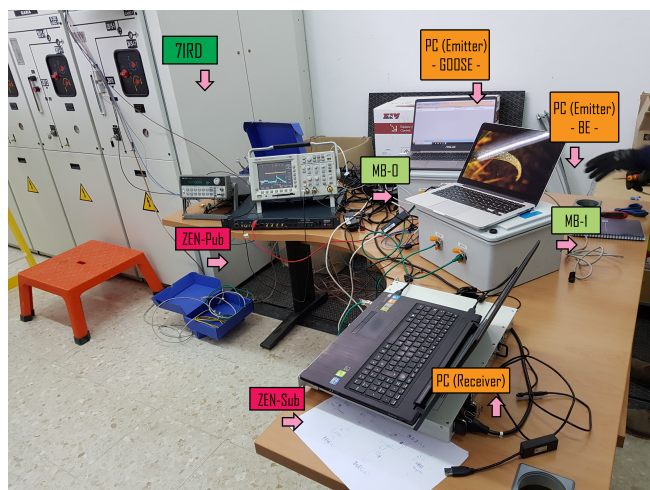


FIGURE 4: Picture of the experimental setup used in the electrical substation where the field tests were conducted, highlighting the actual test equipment, measuring instruments, and the connection to the 7IRD Line Protection Unit.

the other Listener end-system (ZEN-Sub), which regenerates the analog trigger on reception of these messages. The digitalization and analog conversion of the critical messages is supported by the DIO feature of the WR-ZEN nodes. The network also uses two laboratory PCs for producing additional background traffic in the TSN system. These flows will be injected from the Emitter PC and forwarded to Receiver PC over the two MAIN nodes (MB0 and MB1). These flows include GOOSE substation signaling, which is generated with a custom application developed at CIRCE Foundation [30], and Best-Effort traffic, which is emitted with a general-purpose traffic generator [31].

C. COMMUNICATION FLOWS DEFINED FOR THE SUBSTATION FIELD TESTS

Consequently, the following communication flows were established during the experiments:

- **A high priority flow (VLAN Priority: 3/2).** The messages resulting from the digitalization of the analog triggers originating from the 7IRD node which are forwarded from the ZEN-Pub to ZEN-Sub nodes.
- **A medium priority GOOSE signaling stream (VLAN Priority: 1).** The TSN stream between the two laboratory PCs simulating the presence of background substation signaling with the GOOSE protocol.
- **A Best-Effort flow (VLAN Priority: 0).** Bulk Ethernet messages simulating the presence of additional, Internet-like traffic, typically non-critical monitoring.

D. CONFIGURATION PARAMETERS FOR THE SUBSTATION FIELD TESTS

The substation field tests have the objective of comparing the performance of the legacy transmission mechanism of critical events in substation facilities to what is achievable with a TSN Proof-of-Concept system. To this end, two different experiments, whose settings are outlined in Table 1, were devised: *Performance of the Legacy Analog System*, for studying the former, and the *TSN Proof of Concept*, for the latter.

TABLE 1: Configuration parameters applied for the characterization of the analog system and the TSN Proof of Concept. The PCP (Priority Code Point) applied for the VLAN tag of each traffic class also denotes their corresponding TAS queue.

Configuration settings applied for the substation field tests		
Configuration Parameters	Performance of the Legacy Analog System	TSN Proof of Concept
Traffic Classes & Priority	N/A (Analog triggers for signaling critical events)	<ul style="list-style-type: none"> - gPTP: PCP 3 - Critical: PCP 3 - GOOSE: PCP 1 - Best-Effort: PCP 0
Scheduling Policy	N/A	Configuration Set 1 (Table 5)
Network Routing	Analog link from the 7IRD Unit to the Substation Relay	Default Routing in Table 4
Timing Distribution	N/A	ZEN-Pub Node operating as Grand Master for all Nodes in the TSN System

E. EXPERIMENTAL VALIDATION AT THE SUBSTATION FACILITY

This section presents the field tests carried out at the substation facility to demonstrate the feasibility of the application of TSN networks for supporting Smart Grid deployments. These tests translate into the implementation of two major experiments with the goals of characterizing the legacy systems of the substation and showing the potential application of a TSN network in this environment, respectively.

An overview of these experiments is contained in Table 2, where each experiment is introduced, its goals are pre-

sented, its experimental setup and measurements are briefly described, and its corresponding outcome is outlined. The results of each experiment are also discussed in greater detail in the points IV-E1 and IV-E2.

1) Performance of the Legacy Analog System

This experiment was meant to establish the reference, baseline performance of the legacy substation equipment that is used for propagating high priority signals and events. In the substation facility, this mechanism consisted of a dedicated analog link that transmitted critical events in the form of simple analog triggers. These triggers, which originate from the 7IRD Unit, are then delivered to a substation relay that interfaces with the appropriate controller.

Thus, the experiment characterized their propagation time along this analog circuit. As a result, it was found that the transmission latency was around $\sim 209 \mu\text{s}$, as can be observed in Fig. 5a (Left). It can be seen that the dedicated analog link is a reliable means for transporting critical signals; however, this comes at the expense of forfeiting transmission speed, as the circuit incurs additional latency by activating an electromechanical substation relay, and larger deployment costs, as these links have to be set up separately for each type of event handled at the substation.

2) Feasibility of the TSN Proof of Concept

This experiment was used to demonstrate the feasibility of applying a TSN network to support the transmission of critical process data over shared Ethernet interfaces while other substation traffic is also present in the background, such as GOOSE or Best-Effort flows. This was shown with a Proof-of-Concept setup that measured the propagation time of critical events that were digitized and forwarded over the TSN system. It was found that the same critical events originating from the 7IRD Unit could now be delivered within $30 \mu\text{s}$, that is, around $\sim 169 \mu\text{s}$ faster delivery than the legacy analog system. This can be seen in Figs. 5b and 5c (Center and Right). This delivery time corresponds to the propagation time along the three hops of the network.

These results are a significant Proof of Concept of the application of TSN for Smart Grid-capable substations. It is also proof of its scalability and versatility, as multiple flows with different levels of criticality and priority can now be deployed sharing the same physical TSN link, thereby removing the need for costly dedicated analog interfaces with complex electromechanical components that were often required for propagating critical signals.

The level of performance of this TSN flow aggregation mechanism is largely determined by the configuration parameters supplied by the user, which can enormously impact variables such as the packet loss ratio or the end-to-end latency. The influence of these parameters could not be studied at the substation where the TNS PoC demonstrator was deployed, as the electromechanical elements of the 7IRD Unit and the substation relay can only be safely activated a limited number of times before causing excessive wear. Hence, this

TABLE 2: Overview of the experiments at the substation facility characterizing the Legacy System and the TSN PoC.

Experiments performed for the substation field tests				
Configuration Parameters	Objective	Laboratory Setup	Characterization Measurements	Results
Performance of the Legacy Analog System	Assess the performance of the analog-based transmission mechanism at the substation	Injection of single analog trigger from the 7IRD Unit.	Measure time difference between rising edges of analog triggers at points 1b) & 2b) (Fig. 3).	<ul style="list-style-type: none"> – 209 μs TX latency using the legacy analog system. – Fig. 5a (Left). – Discussion in IV-E1.
TSN Proof of Concept	<ul style="list-style-type: none"> – Measure the performance of the TSN System. – Compare against that of the legacy, analog system. 	<ul style="list-style-type: none"> – Inject single analog trigger from the 7IRD Unit into the DIO input of the ZEN-Pub node. – Generate GOOSE from Emitter PC (~0.6 Mbps). – Produce Best-Effort video from the Emitter PC (50 Mbps). 	<ul style="list-style-type: none"> – Measure end-to-end latency for the critical TSN messages as the time difference between 1b) & 2c) (Fig. 3). – Characterize propagation time over the TSN System. 	<ul style="list-style-type: none"> – 30 μs propagation time over the TSN system for critical messages. – Faster than the legacy system with the substation relay (Figs. 5b [Center] and 5c [Right]). – Successful integration of Best-Effort, GOOSE, and critical messages (digitized triggers) over TSN. – Discussion in IV-E2.

characterization was performed thoroughly in a controlled laboratory environment, where the TSN demonstrator was replicated and larger data sets could be compiled.

V. SYSTEM CHARACTERIZATION. LABORATORY EXPERIMENTS AND RESULTS

As introduced at the end of Section IV, different settings can vastly affect the outcome of the data processed and propagated in a TSN system. In light of this, an in-depth characterization of the system was carried out in a controlled laboratory setup that replicated the original environment of the substation. This was accomplished by characterizing the different elements of the system, namely the performance of the timing distribution mechanism or the end-to-end latency and packet-loss ratio attainable under different settings. As a result, it is expected that these experiments will provide some valuable insight into the production of meaningful configuration designs for TSN systems at Smart Grid substations.

A. ELEMENTS OF THE EXPERIMENTAL LABORATORY TEST BENCH

The laboratory test bench presented in this section is intended to replicate that of the electrical substation field tests so that the system can be characterized thoroughly. Thus, the setup used for conducting the laboratory experiments will be similar to that presented in Section IV, but will introduce a few modifications to suit the new laboratory testing environment. These are introduced below.

- **The TSN network nodes.** As before, the TSN network will be formed by two different types of devices: The *End-Systems*, which use the **WR-ZEN nodes**, and the *main switching nodes*, which are implemented with the two **TSN MAIN nodes**.
- **The substation event simulator.** As opposed to the field tests, which used the 7IRD unit for producing critical event analog triggers, the laboratory characterization made use of an off-the-shelf signal generator instead. This signal generator can produce analog triggers at varying rates, which will be used for producing a larger

number of events for the system characterization than could otherwise be generated at the substation environment.

- **The measurement instrumentation.** The experiments characterize the performance of the system in terms of its timing distribution accuracy, propagation delay of critical messages, packet loss ratio, and attainable bandwidth. These two latter parameters will be measured using a regular network sniffing tool [32], while the first two ones will be measured with a TDC Counter instrument [33]. The TDC Counter will replace the oscilloscope given its efficiency to perform multiple back-to-back measurements of end-to-end latency values, which are stored as large data sets that can be used to easily derive statistical indicators.
- **The substation traffic simulators.** The laboratory validation uses the same two laboratory PCs for emitting and receiving both Best-Effort traffic and GOOSE signaling. Traffic integrity statistics will be derived at the Receiver PC.

This experimental setup is only concerned with characterizing the performance of the TSN system and hence the analog transmission circuit found at the substation is not replicated for these experiments. A picture showing the laboratory setup that was built for conducting the experiments presented in this section is included in Fig. 6.

B. CONFIGURATION PARAMETERS FOR THE EXPERIMENTAL CHARACTERIZATION

The laboratory characterization has the goal of determining the effects that the application of different configuration parameters can pose on the achievable determinism of critical messages as well as the integrity of the lower priority flows traversing the network. To this end, five different experiments have been conducted to further analyze the operation of the elements of the TSN system, such as the traffic aggregation mechanism or its timing distribution component, by replicating the environment of the substation field tests in a controlled laboratory setup.

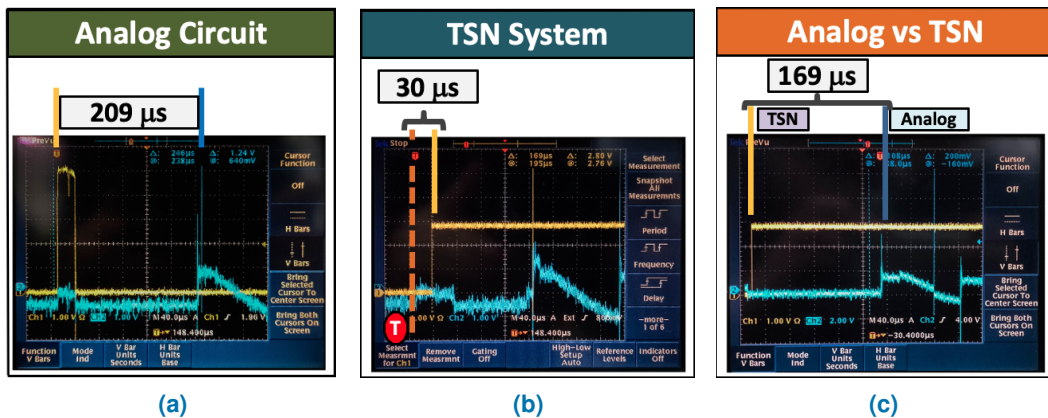


FIGURE 5: Results obtained in the tests carried out at the substation; Figs. 5a (left), 5b (center), and 5c (right). Signal probing points referred to Fig. 3. **Left (Fig. 5a):** Propagation latency through the analog circuit of triggers generated at the 7IRD: $\sim 209 \mu\text{s}$. Yellow trace: injection at (1-b). Blue trace: output at substation relay (2-b). **Center (Fig. 5b):** Propagation delay of digitized critical trigger through the TSN system: $\sim 30 \mu\text{s}$. (T) indicates the oscilloscope trigger at 1-b, the Yellow trace contains the regenerated trigger at 2-c, and the Blue trace shows the output of the analog system at 2-b). **Right (Fig. 5c):** The TSN network propagates event messages around $169 \mu\text{s}$ sooner than the legacy equipment. Yellow trace: regenerated trigger at 2-c. Blue trace: output of the analog system at 2-b).

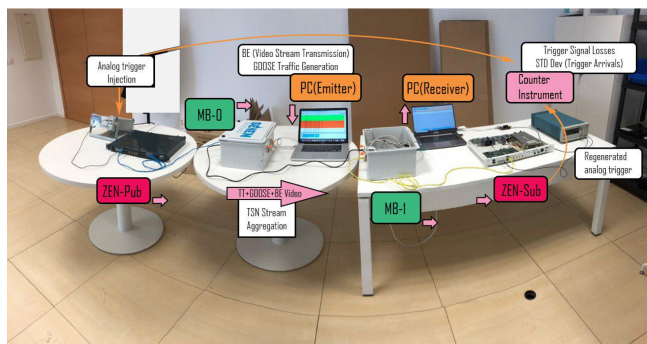


FIGURE 6: Picture of the laboratory setup replicating the substation field test environment. In the image, the network nodes, traffic flows, and connections used for conducting the laboratory validation tests are highlighted.

Each experiment will thus make use of different configuration sets aimed at producing diverse effects in specific aspects of the system; namely on its ability to guarantee timely deliveries of critical messages or the integrity of the lower priority messages. In general, TSN systems need to be supplied with two main sets of parameters for their operation: a set of Traffic Classes for associating different types of traffic with VLAN-tagged TSN streams and a traffic shaping policy (GCL). Traffic classes can be identified in the presented solution by providing specific Ethernet header fields, like the "Destination MAC Address", and an associated priority value (PCP code). In order to minimize resource usage, this implementation was customized to handle priorities ranging from 0 (Best-Effort) to 3 (critical). The traffic shaping policy will be defined by a main scheduling cycle time (T_{cyc}) divided into constituting intervals (I_n) and will have to be applied on the shapers of the egress ports of the nodes in the network. The combined action of the traffic classes and the scheduling

policies are the main drivers of the achievable determinism, but the user also needs to provide routing information for each traffic class and configure at least one of the nodes in the system to operate as a gPTP synchronization Master. Table 3 contains a summary of the configuration parameters that were tapped for each subsystem of the TSN network in order to perform each characterization experiment.

In particular, the Routing settings can be examined in Table 4, which indicates that the critical traffic is exchanged between the ZEN nodes, whereas the GOOSE and Best-Effort flows are exchanged between the two laboratory PCs.

Furthermore, the scheduling policies applied throughout the experiments can be seen in the Tables for the Configuration Sets I (Table 5), II (Table 6), and III (Table 7). Sets I and II share the same structure, as they define a 4 ms periodic schedule divided into three different intervals, with the main difference between the two of them being that Set II enforces a more restrictive policy that limits the transmission of critical frames to a designated slot with a segregated queue (Q_2) from that of gPTP synchronization. Set III studies the effects on the variation of the end-to-end latency of critical messages and the integrity of the rest of the traffic in the network when a number of scheduling policies are iteratively applied to the system.

C. CHARACTERIZATION EXPERIMENTS

This section covers the laboratory characterization that expands on that performed at the substation facility of the TSN system. This is achieved through a series of experiments with a twofold goal. On the one hand, they will aim to characterize the attainable performance and operation of the system (Experiments I through IV), whereas, on the other hand, they will also attempt to delimit the effects that the application of different settings can exert on the achievable determinism of critical TSN flows (Experiment V).

TABLE 3: Configuration parameters applied for performing each experiment in the laboratory validation tests. The PCP (priority) values indicated for the VLAN tag of each type of message also denote their corresponding TAs queue.

Configuration settings applied for the laboratory validation tests					
System Component	Experiment I (Timing)	Experiment II (Baseline)	Experiment III (Highest Attainable Rate)	Experiment IV (Moderate Use)	Experiment V (Worst Case)
Traffic Classes & Priority		gPTP: PCP 3 Critical: PCP 3 GOOSE: PCP 1 Best-Effort: PCP 0		gPTP: PCP 3 Critical: PCP 2 GOOSE: PCP 1 Best-Effort: PCP 0	
Scheduling Policy	All Queues Open All the Time	Configuration Set I		Configuration Set II	Configuration Set III
Network Routing	No TSN flows routed	Default Routing in Table 4			
Timing Distribution	ZEN-Pub Node operating as Grand Master for all Nodes in the TSN System				

TABLE 4: Routing configuration used in the laboratory validation experiments. All the TSN flows are forwarded over the MAIN TSN nodes to the appropriate recipient.

TSN Stream Routing Settings			
Start Node	Hop#0	Hop#1	End Node
ZEN-Pub.[Critical TSN Stream]	MB0	MB1	ZEN-Sub
PC Emitter.[GOOSE Signaling & Best-Effort]			PC Receiver

TABLE 5: Configuration applied to Experiments II (V-C2) and III (V-C3). The table defines a 4 ms periodic cycle divided into three different intervals where the critical flow and the gPTP messages share the same queue (Q3).

Configuration Set I			
Interval No.	Duration (ms)	Queue Settings [Q0 Q1 Q2 Q3]	Description
I0	2	1001	BE & Critical & gPTP
I1	1	0101	GOOSE & Critical & gPTP
I2	1	0001	Critical & gPTP

A general overview of these experiments is presented in Table 8, where each experiment is introduced, along with its goals, the experimental setup that was applied, the measurements that were performed, and the corresponding outcome that resulted from the experiment. These outcomes are subsequently discussed in greater detail in the corresponding discussion sections for each experiment (Sections V-C1 through V-C5).

1) Performance of the Timing Distribution System

The timing distribution is one of the crucial components required to ensure proper operation of a Time-Sensitive Networking system. Thus, its task is to propagate an accurate time base that will be shared amongst all the nodes that are part of the TSN network to guarantee the deterministic forwarding of critical messages, as it enables the synchronous operation of the different time-aware queues throughout the network. Overall, the achievable determinism of the TSN system is limited by the robustness of its timing distribution service, that is, the gPTP component, which is characterized in this experiment.

This characterization was carried out by means of deriving the Allan Deviation (ADEV) [34], which was calculated by recording PPS time differences between the ZEN-Pub and

TABLE 6: Configuration applied to Experiment IV (V-C4). The table defines a 4 ms periodic cycle divided into three different intervals where the critical flow gets its own separate queue (Q2) from the gPTP messages (Q3).

Configuration Set II			
Interval No.	Duration (ms)	Queue Settings [Q0 Q1 Q2 Q3]	Description
I0	2	1001	BE & gPTP
I1	1	0101	GOOSE & gPTP
I2	1	0011	Critical & gPTP

ZEN-Sub nodes for 65 hours. The ADEV indicator was then derived and represented using the *AllanTools* toolset [35], as shown in Fig. 7. The results indicate that the system remains stable in the long term and behaves linearly as its phase noise corresponds to that of a Gaussian process. Furthermore, the plot shows that the timing distribution has a degree of accuracy in the vicinity of tens of nanoseconds (~ 10 ns) for averaging times on the order of one second, which is in the same range as other commercial PTP-based solutions.

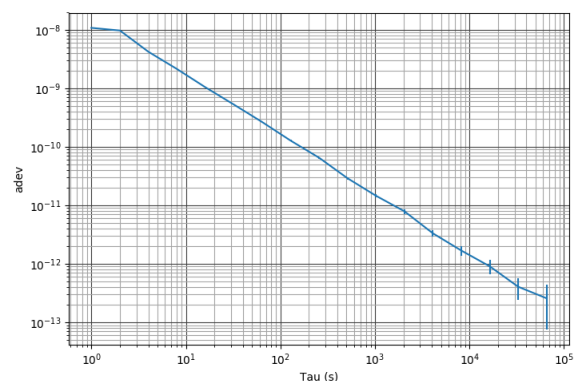


FIGURE 7: Allan Deviation plot showing the degree of accuracy and timing distribution stability calculated for the gPTP synchronization component of the TSN system after recording PPS difference samples for approximately 65 hours.

2) TSN Flow Aggregation over Ethernet Links. Baseline Scenario

This test defines the baseline experimental case as it characterizes the system using a trivial configuration similar to

TABLE 7: Scheduling policy for the iterative sweep of different scheduling cycle times performed during Experiment V (V-C5). Each row corresponds to a specific iteration, which is associated with a given cycle time that is divided into three separate intervals.

Configuration Set III							
Scheduling Policy		Interval 0		Interval 1		Interval 2	
Iteration No.	Total Cycle (μs)	Duration (μs)	Configuration [Q0 Q1 Q2 Q3]	Duration (μs)	Configuration [Q0 Q1 Q2 Q3]	Duration (μs)	Configuration [Q0 Q1 Q2 Q3]
0	48	24	BE & gPTP [1001]	12	GOOSE & gPTP [0101]	12	Critical & gPTP [0011]
1	96	48		24			
2	192	96		48			
3	384	192		96			
4	768	384		192			
5	1536	768		384			
6	3072	1536		768			
7	6144	3072		1536			
8	12288	6144		3072			

that used during the substation field tests. The results can be examined in Tables 9 and 10, which show the transmission latency of critical TSN messages and the level of integrity of the lower priority flows in the network (GOOSE and Best-Effort), respectively.

The experiment measured the operation of the system for 300 seconds and found that the applied scheduling policy yielded minimized end-to-latency for the critical messages transmitted over the network. This minimized latency oscillates between $25.8 \mu\text{s}$, which is associated with the message propagation time, and a peak of $38.7 \mu\text{s}$, which is accounted for by the effect of interfering gPTP traffic sharing the same queue ($Q3$) as the critical traffic, as can be seen in Table 9. Hence, in this experiment the main variable affecting the determinism of the critical flow is its associated priority regardless of the interval distribution in the scheduling cycle, which in this case was set to a 4 ms cycle (T_{cyc}) with a 1 ms interval for the critical traffic (I_2). As the traffic shapers implement a strict priority selection mechanism when several queues are active during the same interval, having the critical flow share the higher priority queue of gPTP (priority 3), which must always be open, would yield this minimized end-to-end latency.

Furthermore, it was shown that this particular policy allows the preservation of the integrity of the GOOSE substation signaling messages at the expense of the Best-Effort traffic, which undergoes minor degradation ($< 1\%$) under this configuration (Table 10).

3) TSN Flow Aggregation over Ethernet Links. Highest attainable rate for critical traffic

The goal of the experiment was to determine the highest generation rate of critical messages under the baseline configuration that allows their transmission without incurring any data losses. This was measured by totalizing the number of critical messages forwarded from the ZEN-Pub node to the ZEN-Sub node, and then detecting packet losses as *non-zero counter totalization*.

It was determined that the highest attainable critical message transmission rate was 670 Hz for this experiment and configuration set in particular, and that higher rates gave rise

to congestion losses. Consequently, the subsequent experiments will generate critical messages at 100 Hz in order to produce larger data sets for deriving statistical indicators.

4) TSN Flow Aggregation over Ethernet Links. Moderate Link Utilization with Best-Effort Traffic.

This experiment studies the effects of the application of a more restrictive scheduling policy, whereby critical messages will now have a separate, designated queue ($Q2$) which only gets activated during a specific interval of the Configuration Set II (Table 6). The synchronization queue ($Q3$) is always active. Thus, it is expected that critical messages will be prone to be severely impacted by the user-designed schedule applied in the experiment. This is shown in Table 11, where it can be seen that the end-to-end latency variation is directly related to the length of the scheduling cycle.

Specifically, this policy still allows the realization of a minimum $\sim 26 \mu\text{s}$ latency, which corresponds to the minimum propagation and processing time through the Ethernet links. However, the maximum end-to-end latency will now be determined by the relationship between the cycle time of the scheduling policy of the Configuration Set II and the length of time that the queue for critical traffic remains idle. Hence, it should be noted that unlike the case of Experiment V-C2, the choice of a priority value other than 3 for the critical messages will result in the fact that the scheduling cycle and its internal interval distribution will now be the decisive factors for establishing the end-to-end latency of a given flow.

This is confirmed in the experimental data, where the $\sim 3 \text{ ms}$ latency variation measured corresponds to the length of time that the critical message queue is inactive, as the applied policy defines a 4 ms cycle (T_{cyc}) with a 1 ms slot when $Q2$ is active (I_2). Besides, as the critical traffic generator is not synchronized to the TSN system time, the $900 \mu\text{s}$ standard deviation of the end-to-latency of the critical messages indicates that most critical frames are forwarded within the 1 ms slot when $Q2$ is active. This leads to the conclusion that a poorly designed schedule could result in catastrophic system operation, causing critical data to miss delivery deadlines or even experience congestion losses.

TABLE 8: Overview of the experiments in the laboratory validation environment.

Experiments performed for the laboratory tests				
Configuration Parameters	Objective	Laboratory Setup	Characterization Measurements	Results
Experiment I (Timing)	Measure the performance of the timing distribution system.	<ul style="list-style-type: none"> ZEN-Pub acting as Timing Master. MB0, MB1, and ZEN-Sub are Timing Slaves. 	<ul style="list-style-type: none"> Measure PPS time difference between the ZEN-Pub and ZEN-Sub nodes (1a) & 2a) in Fig. 3). 	<ul style="list-style-type: none"> Calculation of the Allan Deviation (ADEV) for time synchronization stability. ADEV Plot (Fig. 7). ~10 ns, PTP-like accuracy. Discussion in V-C1.
Experiment II (Baseline)	Characterize the TSN Link Aggregation mechanism with trivial configuration.	<ul style="list-style-type: none"> Inject 1 Hz triggers into the DIO of the ZEN-Pub. Generate GOOSE from Emitter PC (@ ~0.6 Mbps). Produce Best-Effort video from the Emitter PC (4 Mbps). 	<ul style="list-style-type: none"> Measure end-to-end latency variation for the critical flow (1b) & 2c) in Fig. 3). Study level of traffic integrity preservation for the GOOSE and Best-Effort flows. 	<ul style="list-style-type: none"> Critical Traffic Latency in Table 9. Traffic Integrity in Table 10. Verified end-to-end determinism of critical messages and aggregation of other flows (BE, GOOSE). Discussion in V-C2.
Experiment III (Highest Attainable Rate)	Characterize resilience of the TSN Link Aggregation mechanism by determining the highest attainable transmission rate of critical traffic.	<ul style="list-style-type: none"> Incremental sweep on trigger generation rates injected into the DIO of the ZEN-Pub. Generate GOOSE from Emitter PC (@ ~0.6 Mbps). Produce Best-Effort video from the Emitter PC (4 Mbps). 	<ul style="list-style-type: none"> Determine trigger rate that causes critical packet losses during network transmission (1b) & 2c) in Fig. 3). Detected as non-zero totalization condition in the Counter instrument. 	<ul style="list-style-type: none"> Highest achievable rate under current configuration: 670 Hz. Subsequent experiments will generate critical messages at 100 Hz. Discussion in V-C3.
Experiment IV (Moderate Use)	Study the effects of the application of a more restrictive scheduling policy with separate queues for critical and gPTP traffic.	<ul style="list-style-type: none"> Inject 100 Hz trigger signals for producing critical traffic into the DIO input of the ZEN-Pub. Generate GOOSE from Emitter PC (@ ~0.6 Mbps). Produce Best-Effort flow from the Emitter PC (50 Mbps). 	<ul style="list-style-type: none"> Measure impact of new scheduling on the end-to-end latency of critical traffic (1b) & 2c) in Fig. 3). 	<ul style="list-style-type: none"> Critical traffic Latency in Table 11. End-to-end latency as a function of cycle & interval time. Discussion in V-C4.
Experiment V (Worst Case)	In-depth characterization of the influence on determinism of different scheduling designs by applying an iterative sweep of policies with growing cycle times and interval lengths.		<ul style="list-style-type: none"> Measure end-to-end latency of critical traffic for each iteration of Configuration Set III (1b) & 2c) in Fig. 3). Measure integrity of the GOOSE and Best-Effort flows for each iteration of Configuration Set III. 	<ul style="list-style-type: none"> Critical traffic Latency in Table 12. Traffic Integrity in Table 13. Determinism for critical flows can be set by the scheduling policy. Found effects of cycle & interval in (1) for this scenario. Discussion in V-C5.

TABLE 9: Transmission latency associated with the highly critical traffic in the baseline characterization scenario.

Critical Traffic Delivery Jitter			
MAX (μ s)	min (μ s)	Peak-to-Peak (MAX-min) (μ s)	Std.Dev. (μ s)
38.70	25.80	12.938	1.173

5) TSN Flow Aggregation over Ethernet Links. Worst-case End-to-End Latency for Critical Traffic and Effects on the Integrity of Lower Priority Flows.

The tests carried out in this section set out to delimit the influence of different scheduling policies on the attainable determinism for critical messages and the integrity of the lower priority flows in the network. This was achieved by applying the different policies defined in each iteration of the Configuration Set III (Table 7).

The results of the end-to-end latency for the critical messages under each iteration can be examined in Table 12, where it can be seen that the relationship determining the maximum latency variation between the schedule cycle time and the duration of the slot for critical traffic that was pointed out in V-C4 still holds, and can be described with

the expression in (1).

$$Lat_{max} = T_{cyc} - I_2 + t_{prop} + t_{del} \quad (1)$$

This expression describes the effect that the configuration applied for the experiment has on the latency variation. Hence, it was found that the maximum end-to-end latency was determined by the duration of the scheduling cycle (T_{cyc}) and the length of the slot for critical traffic (I_2), with the additional contributing delays of the propagation time (27 μ s) through the Ethernet links of the TSN system (t_{prop}), and a peak processing time of 17 μ s (t_{del}). This empirical derivation of (1) is meant to show that the user would be able to adjust the end-to-end determinism by supplying different values for T_{cyc} and I_2 for the scenario under evaluation with the current choices of priority for the critical flow and network topology. For instance, a 384 μ s cycle (T_{cyc}) combined with a 96 μ s interval for the critical traffic (I_2) should yield a maximum latency of 332 μ s, which closely resembles the experimental data for *iteration 3* in Table 12 (MAX: 332.210 μ s). Other scenarios with different network topologies or a greater number of flows and priorities might lead to different expressions that the user should evaluate and leverage to design configurations that can target the desired

TABLE 10: Summary of the traffic integrity level for the medium priority and Best-Effort flows traversing the network in experiment V-C2, as indicated in the Packet Loss (PL) entries. In the table *NTX* and *NRX* indicate the number of packets sent/received, and *BW(TX)* and *BW(RX)* indicate the measured bandwidth on transmission/reception. The integrity of the medium priority GOOSE signaling is preserved at the expense of the Best-Effort messages, which undergo minor degradation (< 1%).

Cycle (μ s)	GOOSE				Best-Effort (BE)				Results	
	NTX [pkets]	NRX [pkets]	BW(TX) [Mbps]	BW(RX) [Mbps]	NTX [pkets]	NRX [pkets]	BW(TX) [Mbps]	BW(RX) [Mbps]	GOOSE PL (%)	BE PL (%)
4000	129493	129493	0.573	0.573	698250	693252	3.9	3.8	0	0.716

TABLE 11: Transmission latency associated with the critical traffic from experiment V-C4. The test uses a segregated queue for the critical traffic (*Q2*) and a long scheduling cycle with a small service slot for *Q2*, resulting in significant end-to-end latency variation (~ 3 ms).

Critical Traffic Delivery Jitter			
MAX (μ s)	min (μ s)	Peak-to-Peak (MAX-min) (μ s)	Std.Dev. (μ s)
3030	26	2999.013	900.075

TABLE 12: Values for the end-to-end latency associated with the critical flow obtained for each iteration defined in Table 7.

Critical Traffic Delivery Jitter for each Iteration					
It. No.	Cycle (μ s)	MAX (μ s)	min (μ s)	Peak-to-Peak (MAX-min) (μ s)	Std.Dev. (μ s)
0	48	81.225	27.101	54.124	13.814
1	96	117.201	27.019	90.182	25.168
2	192	187.464	27.052	160.412	50.147
3	384	332.210	27.082	305.128	97.818
4	768	619.064	27.053	592.01	193.117
5	1536	1192.346	27.085	1165.261	382.413
6	3072	2340.611	27.072	2313.539	764.557
7	6144	4644.351	27.057	4617.294	1525.328
8	12288	9251.809	26.992	9224.817	3050.670

levels of determinism.

The level of integrity of the lower priority flows in the network for each iteration of the experiment was also examined in parallel, as shown in Table 13. These results have a twofold implication. On the one hand, the determinism of critical flows is a parameter that can be designed for to meet the requirements of a given system (e.g., delivery deadlines). On the other hand, there is a compromise with the integrity of the lower priority flows, which could experience congestion losses with growing cycle times and comparatively short service slots. This is the case of the iterations under study, where the Best-Effort can undergo significant degradation in some cases, while the GOOSE signaling remains protected given its lower bandwidth usage and higher priority.

It is important to note that the buffer depth (4 kB per queue) of the current implementation is an important factor in the integrity results. The Best-Effort traffic is generated at a constant rate of 50 Mbps using 1500 B frames and, since a general-purpose OS is used, occasional bursts may occur. As the Best-Effort queue can only hold two 1500 B frames at a time when it is idle, we have found that there are cycle/interval combinations where the occasional frame may be dropped (~ 4 frames on average) for cycles shorter

than 1536 μ s, and others, like 768 μ s, that manage to avoid dropping any messages altogether. GOOSE traffic uses a higher priority queue with frames of 168 B (26 messages per queue), but it could still be affected if the traffic were to be emitted in sufficiently large bursts. This was the case of the 192 μ s cycle iteration. These effects would be mitigated using larger buffers.

Lastly, it is worth noting that the application of the scheduling policies of Set III produces the maximum end-to-end latency described in the expression in (1) when applied to the current system. However, more complex topologies with a greater number of flows may require the derivation of more complicated policies. These policies should be able to take into account the fact that TSN messages may have different associated forwarding times as a result of internal processing delays, impact of arbitration mechanisms, or the use of redundant paths (802.1CB). This latter case is especially sensitive, as the propagation times of redundant messages over different paths can be widely different. In the literature there are several works for calculating meaningful policies under such complex scenarios, and the work in [36] provides a useful framework for assessing the synchronization error in message forwarding associated with the application of a certain policy in systems with time-varying delays.

VI. CONCLUSIONS AND FUTURE WORK

This work has shown the feasible application of a TSN system to a substation environment to enhance the new Ethernet-based networks that are being deployed in these facilities during the transition to Smart Grid. It is expected that the use of Smart Grid will provide a unified framework for managing and handling all the different data flows of the substation. Ethernet-based networks already allow the transmission of the signaling data and non-critical messages that are usually propagated in these environments. In this context, critical event data messages, which cannot be handled by regular Ethernet networks, could benefit from the use of the proposed TSN system, which would allow their deterministic transmission alongside the other flows of the substation over the same Ethernet-based infrastructure. Hence, this would provide Smart Grid-enabled substations with a flexible networking stack allowing simultaneous propagation of ordinary IP flows, GOOSE signaling (IEC 61850), or the critical traffic typically found in the supervisory and control processes on the Bay Level equipment of the substation.

This latter point was proven by devising and deploying a Proof-of-Concept TSN system in an actual electrical substa-

TABLE 13: Summary of the traffic integrity level for the medium priority and Best-Effort flows traversing the network throughout the iterations of experiment V-C5, as indicated in the Packet Loss (PL) entries. In the table, *NTX* and *NRX* indicate the number of packets sent/received, and *BW(TX)* and *BW(RX)* indicate the measured bandwidth on transmission/reception. GOOSE signaling is preserved, even though some of the policies applied cause significant degradation for Best-Effort traffic (> 20%).

Cycle (μ s)	GOOSE				Best-Effort (BE)			Results (Packet Losses %)		
	NTX [pkts]	NRX [pkts]	BW(TX) [Mbps]	BW(RX) [Mbps]	NTX [pkts]	NRX [pkts]	BW(TX) [Mbps]	BW(RX) [Mbps]	GOOSE PL (%)	BE PL(%)
48	24371	24371	0.099	0.098	1233030	1233026	49	49	0	0.000324
96	31549	31549	0.129	0.129	1232509	1232505	49	49	0	0.000325
192	32298	32296	0.133	0.132	1206781	1206777	49	49	0.006192	0.000332
384	142503	142503	0.577	0.577	1234170	1234169	49	49	0	$8.1026 \cdot 10^{-5}$
768	142309	142309	0.577	0.577	1235835	1235835	49	49	0	0
1536	141115	141115	0.569	0.568	1228597	1228593	49	49	0	0.000326
3072	141771	141771	0.577	0.576	1241574	1013543	49	40	0	18.366283
6144	141044	141044	0.577	0.577	1229649	809817	49	32	0	34.142426
12288	139467	139467	0.576	0.575	1233620	716424	49	28	0	41.925066

tion from a local power utility [28], where several field tests managed to show that a TSN system could be successfully applied to unifying all the communications in the substation over a shared Ethernet-based bus: critical messages carrying digitized trigger data, medium priority GOOSE signaling from the IEC 61850 standard, and Best-Effort flows.

Next, this work performed an in-depth characterization of the influence of different configuration parameters on the performance of the system in a controlled laboratory environment. Thus, this stage started with the evaluation of the gPTP synchronization, which was pegged to the tens of nanoseconds. After that, the ability of the TSN system to combine background substation traffic (GOOSE, Best-Effort) with critical flows was assessed, and it was verified that a deterministic delivery for the critical messages could be enforced. In this context, the influence of the scheduling policy on the end-to-end latency of the critical flows was determined to be the result of the combined effect of the application of different cycle times and interval durations in the traffic shapers: bounded latencies between 81μ s and 9.251 ms could be achieved with cycles (T_{cyc}) and critical intervals (I_2) between 48μ s and 12 ms. Hence, it was found that the design of the traffic-shaping schedule is the chief parameter governing the attainable determinism for a TSN flow, allowing the user to target application-specific requirements. Overall, this has shown that the system is highly versatile and scalable, given its multiple user configuration options for handling different types of traffic, and flexible enough to allow the deployment of distributed applications supported with highly accurate gPTP synchronization (tens of nanoseconds accuracy).

After this characterization work, we have planned a number of future actions, like the development of a user-directed utility for the centralized configuration of the entire system or the design of larger TSN switches. Further applications of TSN for Smart Grid domains could also be considered, like the definition of a Smart Grid Profile for TSN, its application for monitoring low-voltage grids [37], or the implementation of an OPC-UA interface over TSN for managing substation equipment.

REFERENCES

- [1] Time-Sensitive Networking (TSN) Task Group, IEEE [Online]. Available: <https://1.ieee802.org/tsn/>.
- [2] H. Farhangi *et al.*, "The path of the smart grid," IEEE Power Energy Mag., vol. 8, pp. 18–28, 12 2009. DOI: 10.1109/mpe.2009.934876.
- [3] "Red21," Red Eléctrica de España. [Online], Dec. 2018. Available: <https://www.ree.es/es/red21/redes-inteligentes/que-son-las-smartgrid>.
- [4] R. E. Mackiewicz, "Overview of IEC 61850 and benefits," in IEEE PES Power Syst. Conf. and Expo., pp. 623–630, IEEE, 10 2006. DOI: 10.1109/PSC.2006.296392.
- [5] J. L. Gutiérrez-Rivas, J. López-Jiménez, E. Ros and J. Díaz, "White Rabbit HSR: A Seamless Subnanosecond Redundant Timing System With Low-Latency Data Capabilities for the Smart Grid," IEEE Trans. Ind. Informat., vol. 14, no. 8, pp. 3486–3494, 2018. DOI: 10.1109/TII.2017.2779240.
- [6] IEEE Standard for Local and metropolitan area networks—Frame Replication and Elimination for Reliability, IEEE 802.1CB-2017, 10 2017.
- [7] J. Sanchez-Garrido *et al.*, "TSN en Smart Grids – Comunicaciones Deterministas para Operaciones Críticas," in VI Congreso Smart Grids, Madrid, Spain, 2019, pp. 36–41, 2019. [Online] Available: <https://static.smartgridsinfo.es/media/2020/01/6-congreso-smart-grids-libro-comunicaciones.pdf>.
- [8] H. Lei, C. Singh, and A. Sprintson, "Reliability Modeling and Analysis of IEC 61850 Based Substation Protection Systems," IEEE Trans. Smart Grid, vol. 5, pp. 2194–2202, Sep. 2014. DOI: 10.1109/TSG.2014.2314616.
- [9] Audio Video Bridging Task Group, IEEE [Online] Available: <http://grouper.ieee.org/groups/802/1/pages/avbridges.html>.
- [10] IEEE Standard for Local and Metropolitan Area Networks – Timing and Synchronization for Time-Sensitive Applications in Bridged Local Area Networks, IEEE 802.1AS-2011, 3 2011.
- [11] Communication networks and systems for power utility automation - Part 5: Communication requirements for functions and device models, IEC 61850-5, IEC (International Electrotechnical Commission), 1 2013.
- [12] P. Risbud, N. Gatsis, and A. Taha, "Vulnerability Analysis of Smart Grids to GPS Spoofing," IEEE Trans. Smart Grid, vol. 10, pp. 3535–3548, July 2019. DOI: 10.1109/TSG.2018.2830118.
- [13] IEEE Standard for Local and metropolitan area networks – Bridges and Bridged Networks - Amendment 25: Enhancements for Scheduled Traffic, IEEE 802.1Qbv-2015, 3 2016.
- [14] IEEE Standard for Local and metropolitan area networks – Bridges and Bridged Networks – Amendment 26: Frame Preemption, IEEE 802.1Qbu-2016, 8 2016.
- [15] IEEE Standard for Ethernet Amendment 5: Specification and Management Parameters for Interspersing Express Traffic, IEEE 802.3br-2016, 10 2016.
- [16] IEEE Standard for Local and Metropolitan Area Networks—Bridges and Bridged Networks – Amendment 31: Stream Reservation Protocol (SRP) Enhancements and Performance Improvements, IEEE 802.1Qcc-2018, 10 2018.
- [17] IEEE Standard for Local and metropolitan area networks—Audio Video Bridging (AVB) Systems, IEEE 802.1BA-2011, 9 2011.
- [18] IEEE Standard for Local and Metropolitan Area Network—Bridges and Bridged Networks, IEEE 802.1Q-2018, 7 2018.
- [19] "Zynq-7000 SoC Data Sheet: Overview," Xilinx, 7 2018. [Online]. Available:

https://www.xilinx.com/support/documentation/data_sheets/ds190-Zynq-7000-Overview.pdf.

[20] *RTEMS Real-Time Operating System (RTOS) Site*, The RTEMS Project. [Online]. Available: <https://www.rtems.org/>.

[21] WR-ZEN TP Product Site, Seven Solutions, 2019. [Online]. Available: <https://sevensols.com/index.php/products/wr-zen-tp/>.

[22] *White Rabbit Project*, Open Hardware Repository. [Online]. Available: <https://www.ohwr.org/project/white-rabbit/wikis/home>.

[23] Alex Forencich, *Verilog Ethernet Components. A collection of 1G, 10G, 25G Packet Processing Data Paths*. [Online]. Available: <https://gitlab.com/alex.forencich/verilog-ethernet>.

[24] N. Moreira, J. Lázaro, U. Bidarte, J. Jimenez, and A. Astarloa, "On the Utilization of System-on-Chip Platforms to Achieve Nanosecond Synchronization Accuracies in Substation Automation Systems," *IEEE Trans. Smart Grid*, vol. 8, pp. 1932–1942, July 2017. DOI: 10.1109/TSG.2015.2512440.

[25] *OpenAvnu Project: an Avnu sponsored repository for Audio/Video Bridging and Time Sensitive Networking technology*, Avnu Alliance. [Online]. Available: <http://avnu.github.io/OpenAvnu/>.

[26] *Linux AXI Ethernet Driver*, Xilinx. [Online]. Available: <https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/18842485/Linux+AXI+Ethernet+driver>.

[27] D. Mashima, P. Gunathilaka, and B. Chen, "Artificial command delaying for secure substation remote control: Design and implementation," *IEEE Trans. Smart Grid*, vol. 10, pp. 471–482, Jan 2019. DOI: 10.1109/TSG.2017.2744802.

[28] Grupo Cuerva S.L. Electrical Utilities. Official Site (English Version), Grupo Cuerva. [Online]. Available: <https://www.grupocuerva.com/en/>.

[29] ZIV Automation Document Library, ZIV Automation. [Online]. Available: <https://www.zivautomation.com/downloads/#t2-p7>.

[30] CIRCE Foundation for Renewable Energy. Official Site, CIRCE Foundation. [Online]. Available: <http://www.fcirce.es/que-es-circe>.

[31] *PackETH Ethernet Packet Generator Project website*, PackETH. [Online]. Available: <http://packeth.sourceforge.net/packeth/Home.html>.

[32] *Official Wireshark Site*, Wireshark. [Online]. Available: <https://www.wireshark.org/>.

[33] "53230A RF Counter. Brochure for the 53200 Series of RF and Universal Frequency Counter/Timers," Keysight. [Online]. Available: <https://www.keysight.com/us/en/assets/7018-02654/technical-overviews/5990-6339.pdf>.

[34] M. Lombardi, "Fundamentals of Time and Frequency," in *The Mechatronics Handbook*, ch. 17, CRC Press, 01 2002. DOI: 10.1201/9781420037241.ch10.

[35] Anders E.E. Wallin, Danny Price, Cantwell G. Carson, Frédéric Meynadier, Yan Xie, Erik Benkler. *AllanTools. A python library for calculating Allan deviation and related time & frequency statistics*. [Online]. Available: <https://github.com/awallin/allantools>.

[36] B. Wang, W. Chen, B. Zhang, and Y. Zhao, "Regulation cooperative control for heterogeneous uncertain chaotic systems with time delay: A synchronization errors estimation framework," *Automatica*, vol. 108, p. 108486, 2019. DOI: <https://doi.org/10.1016/j.automatica.2019.06.038>.

[37] S. Lu, S. Repo, D. D. Giustina, F. A. Figuerola, A. Löf, and M. Pikkarainen, "Real-Time Low Voltage Network Monitoring – ICT Architecture and Field Test Experience," *IEEE Trans. Smart Grid*, vol. 6, pp. 2002–2012, July 2015. DOI: 10.1109/TSG.2014.2371853.

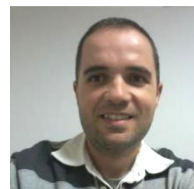


A. JURADO. Antonio Jurado received a Degree in Computer Engineering from the University of Cordoba (Spain) in 2016, and then he got his M.D. in Data Science and Computer Engineering from the University of Granada (Spain) in 2018, where he has been working as a research assistant since 2017 and is currently pursuing his PhD on Information Technologies. His interests include particle accelerator control systems, White Rabbit timing and deterministic networks for Smart Grid.



L. MEDINA. Luis Medina received the B.E. degree in electrical engineering in 2011, the M.Tech in 2014, and his PhD in Electronics (CSIC grant) in 2018 from the University of Alcalá (Spain). He was R&D Engineer in 2015 at U. Alicante (Spain) (U.I of Computer Research). He joined Seven Solutions (Granada, Spain) in 2018, working on TSN (aerospace, particle accelerators) and as technical manager for EU FitOpTiVis. Interests in deterministic networks, RT FPGA design, image and

signals processing.



R. RODRIGUEZ. Mr. Rafael Rodríguez became a Computer Engineer in 2005 and worked as a researcher on different EU projects through 2010, while gaining experience in FPGA computer systems. Cofounder and CTO of Seven Solutions, where he was appointed senior Engineer in 2011. Involved in systems of projects such as ExoMars, particle accelerators (CERN), radio astronomy (SKA), Metrology Institutes (UK). Specialized in timing and open embedded platforms.



E. ROS. Eduardo Ros received his M.Sc. and Ph.D. degrees in Physics from the U. Granada (Spain) in 1992 and 1997 respectively. He is currently Full Professor in the Dept. of Computer Architecture and Technology of U. Granada. He has participated in many international projects as PI (such as SENSOPAC, FITOPTIVIS, etc.). He has authored more than 90 publications in his main areas of interest, including spiking neural networks, White Rabbit timing, or neuroscience.



J. SANCHEZ-GARRIDO. Jorge Sánchez Garrido received his BSc in Telecommunications Engineering in 2013 and his Master's Degree in Data Science and Computer Architecture in 2016, both from the University of Granada (Spain). He previously worked as a consultant at Deloitte Spain (2014-2015), and then started a PhD Programme on Time-Sensitive Networking in 2015 at the University of Granada with interests in White Rabbit timing, deterministic networks, Smart Grid, and FPGA-based systems design. He has been an active researcher in the EU FITOPTIVIS project and several transfer projects into the industry.



J. DIAZ. Javier Díaz received his Ph.D. in electronics in 2006 from the University of Granada, where he combines his academic work as Full Professor with transfer activities as cofounder of Seven Solutions (www.sevensols.com). His main interests are image processing, safety-critical systems, time synchronization and frequency distribution. He collaborates with multiple facilities (CERN, CTA, ...) researching on deterministic communications and White Rabbit technology.

...